FIG 1A

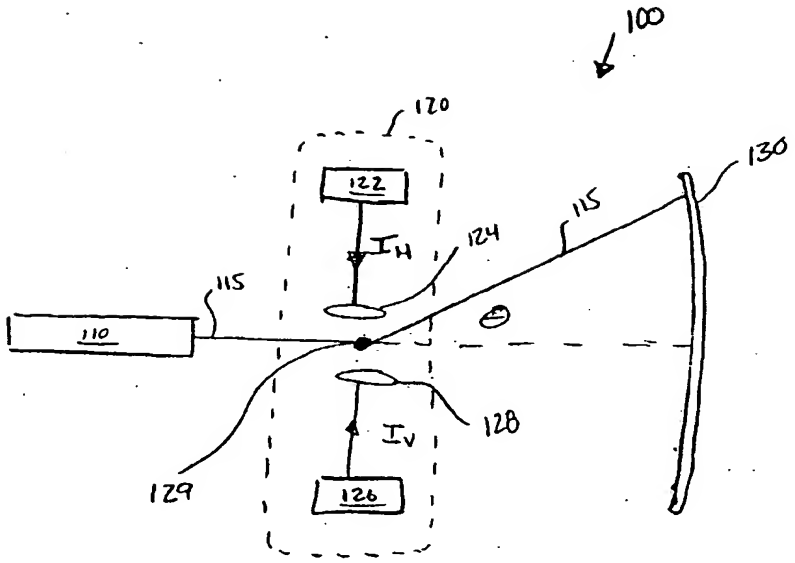

100
120
122
I_H
124
115
130
115
110
θ
128
I_V
129
126

FIG. 1B

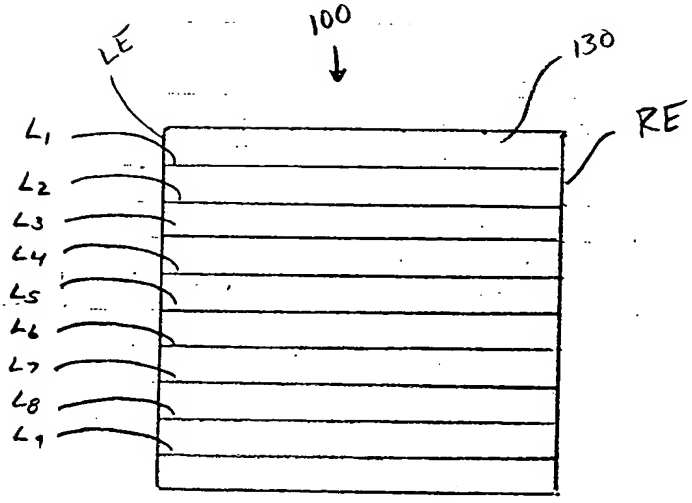

100
LE
130
RE
L_1
L_2
L_3
L_4
L_5
L_6
L_7
L_8
L_9

FIG. 2A

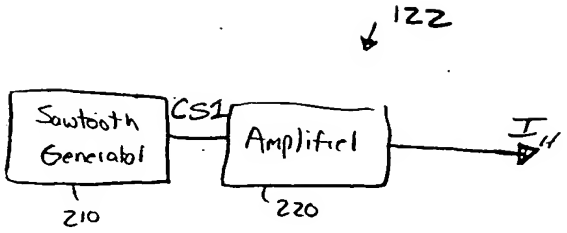


FIG. 2B

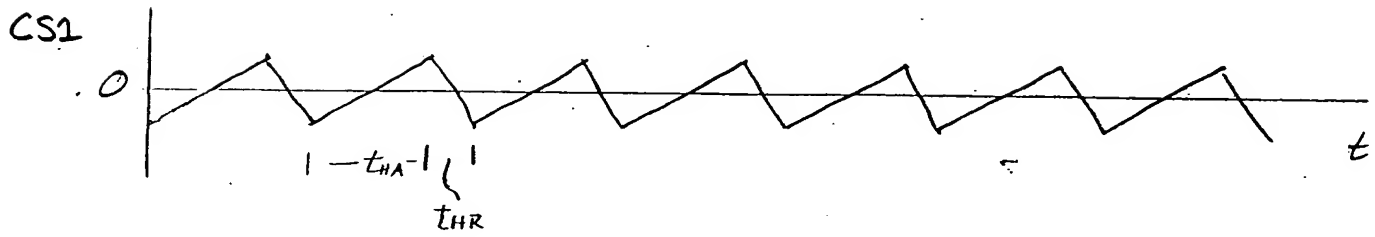


FIG. 2C

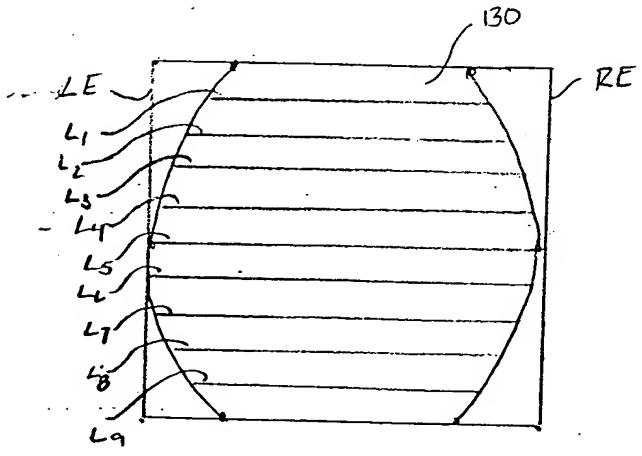


FIG. 3A

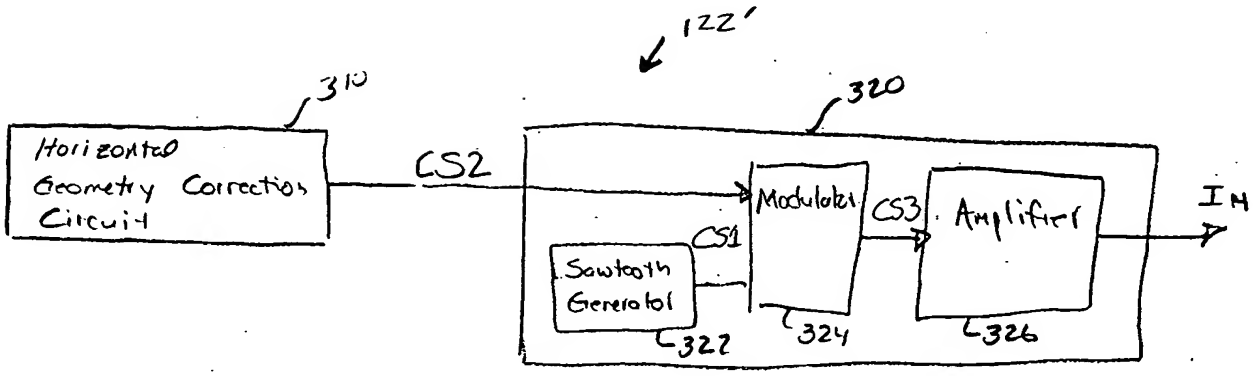


FIG. 3B

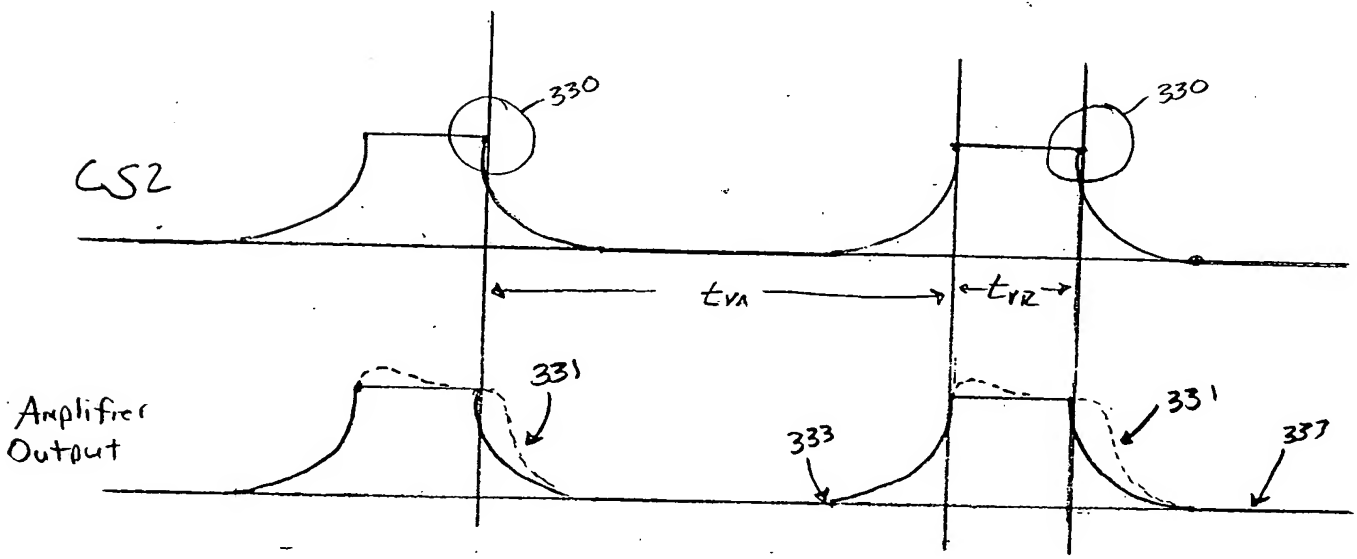


FIG. 3C

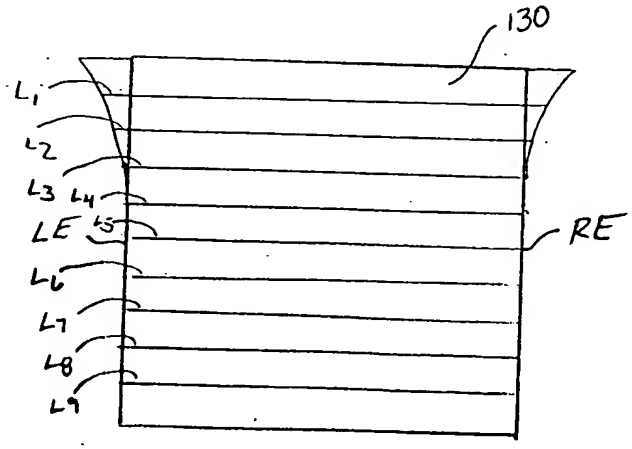


FIG. 4

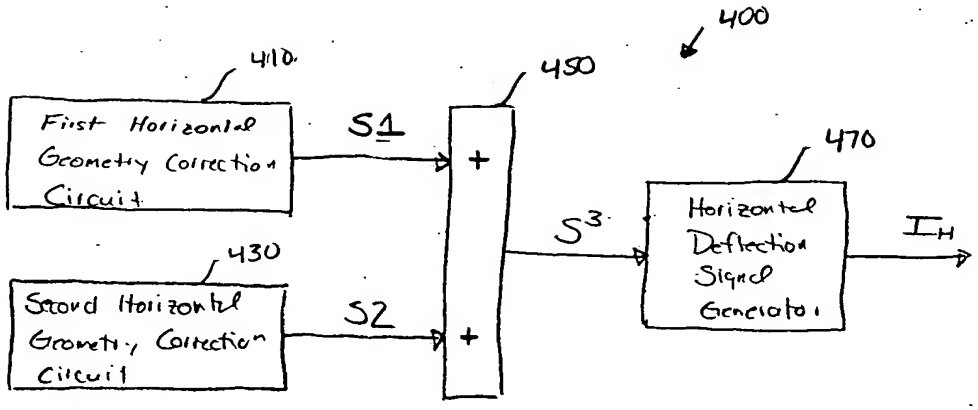

400
410
First Horizontal Geometry Correction Circuit
S1
450
+
470
Horizontal Deflection Signal Generator
S^3
I_H
430
Second Horizontal Geometry Correction Circuit
S2
+

FIG. 5

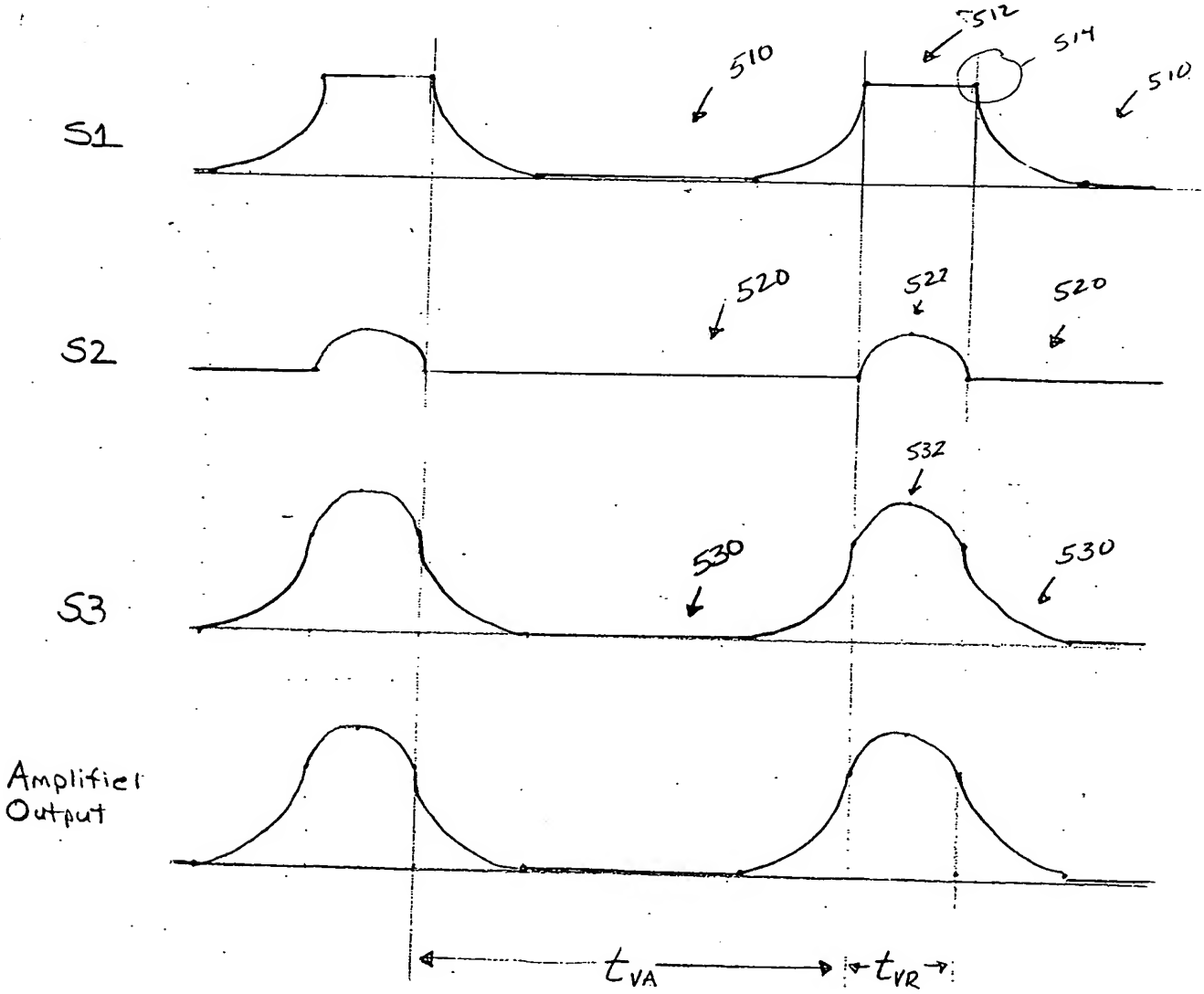
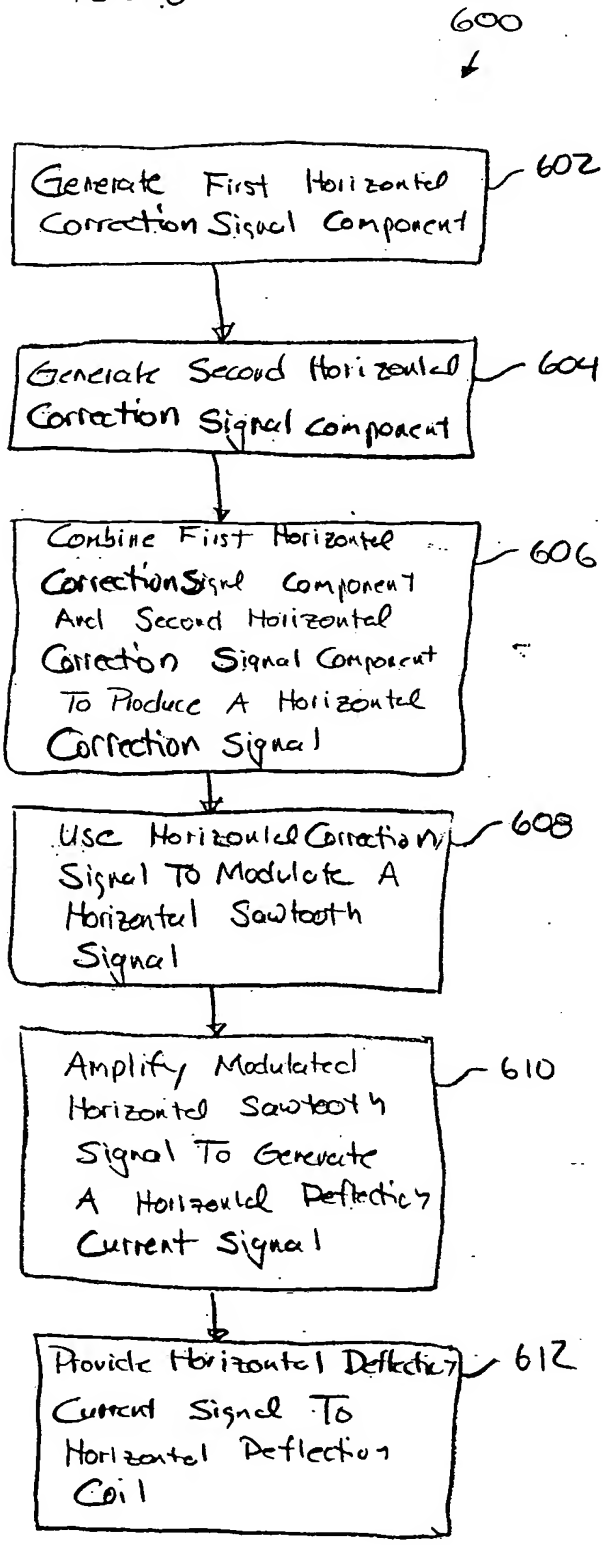

S1
S2
S3
Amplifier Output
510
512
514
520
522
530
532
t_{VA}
t_{VR}

FIG 6


600
Generate First Horizontal Correction Signal Component
602
Generate Second Horizontal Correction Signal Component
604
Combine First Horizontal Correction Signal Component And Second Horizontal Correction Signal Component To Produce A Horizontal Correction Signal
606
Use Horizontal Correction Signal To Modulate A Horizontal Sawtooth Signal
608
Amplify Modulated Horizontal Sawtooth Signal To Generate A Horizontal Deflection Current Signal
610
Provide Horizontal Deflection Current Signal To Horizontal Deflection Coil
612

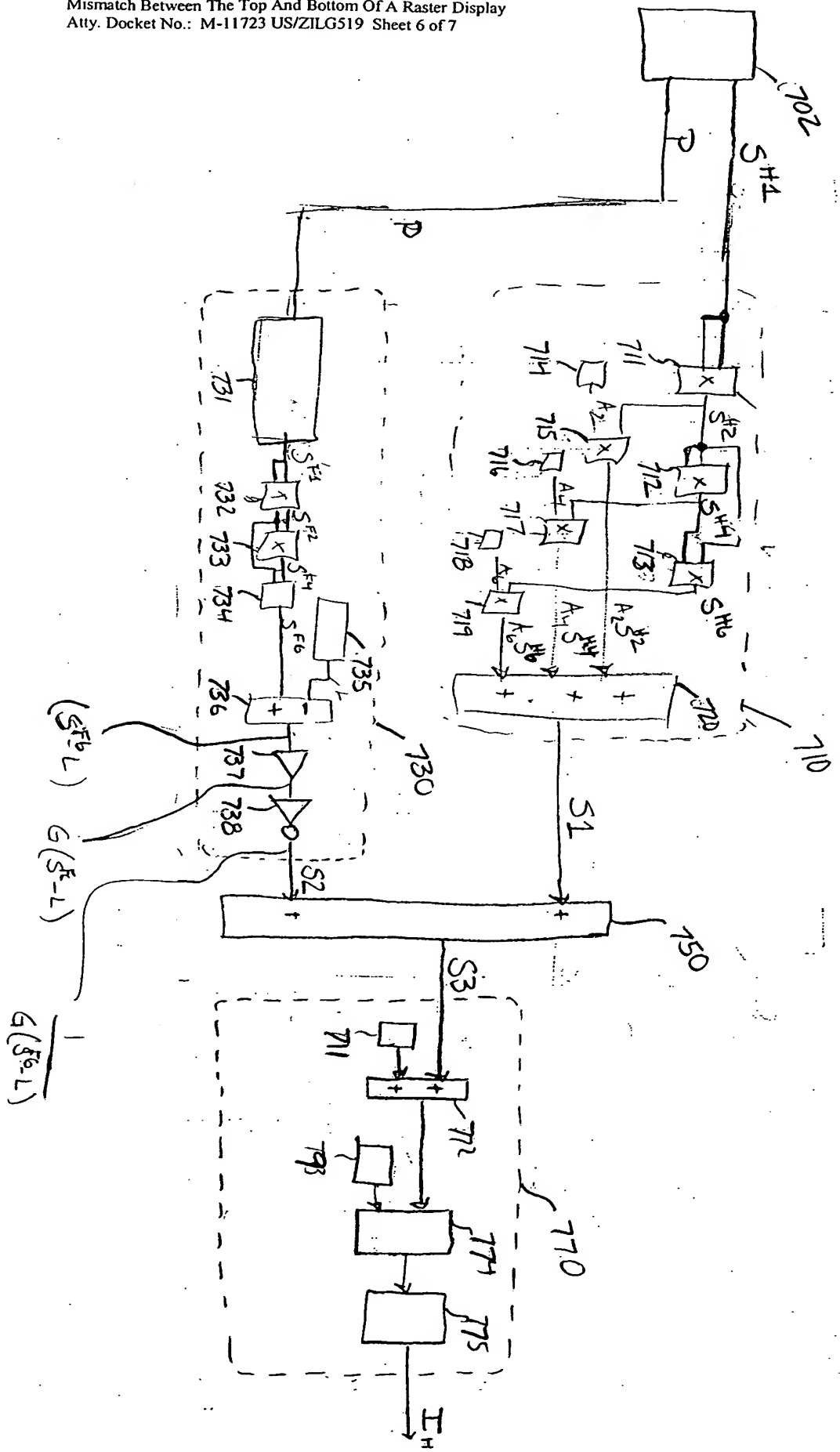

702
P
P
S_{H1}
711
714
715
716
712
717
718
713
719
720
710
731
732
733
734
735
736
737
738
730
S_{F6} (L)
G (S_{F6}-L)
$\overline{G(S_{F6}\text{-}L)}$
S1
S2
750
S3
771
772
773
774
775
770
I_H

FIG. 7

700

FIG. 8